

REMARKS

Entry of Amendment

As Applicants are filing a RCE herewith, this amendment and Response (M) filed April 29, 2009 should be entered and considered by the Examiner at this time.

Allowance

Applicants appreciate the Examiner's allowance of Claims 21-24, 76, 77, 85-90, 93, 94, 97 and 98.

Applicants are amending Claim 76 herein. It is respectfully submitted that this amendment should not affect the allowability of this claim, and it is respectfully requested that this claim continue to be allowed.

Applicants have the following response to the Examiner's remaining rejections.

Claim Rejections – 35 USC §112, Second Paragraph

In the Final Rejection, the Examiner rejected Claims 91, 92, 95, 96 and 99-102 under 35 USC §112, second paragraph, as being indefinite. Applicants provided a response to this rejection in Response (M) filed April 29, 2009, which is incorporated herein by reference. In the Advisory Action of May 7, 2009, the Examiner states that the response overcomes this rejection.

Accordingly, it is respectfully requested that this rejection be withdrawn.

Claim Rejections – 35 USC §112, First Paragraph

In the Final Rejection and the Advisory Action, the Examiner continues to reject Claims 91, 92, 95, 96 and 99-102 under 35 USC §112, first paragraph, as failing to comply with the written description requirement. This rejection is respectfully traversed.

While Applicants traverse this rejection, in order to advance the prosecution of this application, Applicants are amending Claim 91 to delete the claim language “wherein said first insulating film is formed on said source wiring.” It is respectfully submitted that as amended, Claim 91 is clearly shown in Fig. 16 of the present application, as for example shown below (numbers in parenthesis refer to reference numerals in Fig. 16):

91. An electro-optical device comprising:
a first substrate (1000);
a thin film transistor (700) formed over said first substrate (1000), said thin film transistor (700) comprising;
a gate electrode (1001);
a first insulating film (1002) formed on said gate electrode (1001);
a semiconductor layer (1003) formed over said first insulating film (1002), and having a channel formation region (in 1003);
a source region (1004) and a drain region (1005) formed over the semiconductor layer (1003);
a source wiring (1023) formed over said first substrate (1000), the source wiring (1023) electrically connected to said source region (1004) through a second wiring (1006);
a second insulating film (1009) over said semiconductor layer (1003), the source region (1004), the drain region (1005) and the second wiring (1006), wherein the second insulating film (1009) is in contact with the channel formation region (in 1003);
a pixel electrode (1008) comprising a first transparent conductive film, and electrically connected to said drain region (1005);
a second substrate (1010) opposed to said first substrate (1000);
at least a first colored layer (1011) and a second colored layer (1012) formed on said second substrate (1010), wherein said first colored layer (1011) and said second colored layer (1012) partly overlap each other (shown in Fig. 16);
an organic resin film (1013; page 12, lines 3-9) covering said first and second colored layers (1010; 1012);
an opposing electrode (1014) comprising a second transparent conductive film; and
a liquid crystal (1016) interposed between said pixel electrode (1008) and said opposing electrode (1014), wherein said organic resin film (1013) is interposed between said liquid crystal (1016) and said first and second colored layers (1010; 1012),

wherein said organic resin film (1013) has a thickness of 1 μm or more (e.g. page 12, lines 3-9), and

wherein said opposing electrode (1014) is interposed between said liquid crystal (1016) and said organic resin film (1013).

Amended Claim 92 is supported in a similar manner.

Therefore, there is a clear written description of the subject matter of independent Claims 91 and 92. Accordingly, it is respectfully requested that this rejection be withdrawn.

Conclusion

It is respectfully submitted that the present application is in a condition for allowance and should be allowed.

If any further fee should be due for this amendment and/or the RCE, please charge our deposit account 50/1039.

Favorable reconsideration is earnestly solicited.

Date: May 29, 2009

Respectfully submitted,

/Mark J. Murphy/

Mark J. Murphy

Registration No. 34,225

COOK ALEX LTD.
200 West Adams Street
Suite 2850
Chicago, Illinois 60606
(312) 236-8500

Customer No. 26568